

07-11-02

Gp/2122

Docket No. 4362-4002

116

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hinsley et al.

Serial No.: 10/084,780

Filed: February 25, 2002

For: TRANSLATING AND EXECUTING OBJECT ORIENTED COMPUTE
PROGRAMS

Group Art Unit: 2122

Examiner: To Be Assigned

Technology Center 2100

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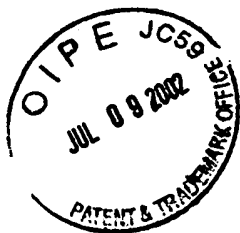
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Commissioner for Patents
Washington, DC 20231

Sir:

In the matter of the above-identified application and under the provisions of 35 U.S.C. §119 and 37 C.F.R. §1.55, applicant(s) claim(s) the benefit of the following prior application(s):

Application(s) filed in: Great Britain
In the name of: TAO Group Limited
Serial No(s): GB 9920676.5
Filing Date(s): September 1, 1999

- ☒ Pursuant to the Claim to Priority, applicant(s) submit(s) a duly certified copy of said foreign application.
- ☐ A duly certified copy of said foreign application is in the file of application Serial No. _____, filed _____.

Respectfully submitted,
MORGAN & FINNEGAN, L.L.P.

Dated: July 9, 2002

By: _____

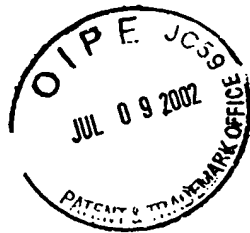
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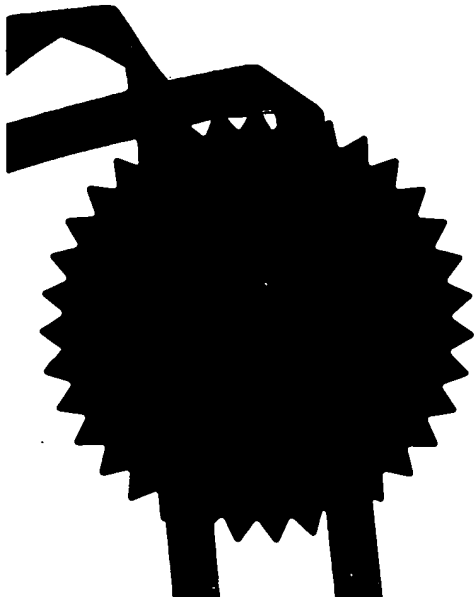
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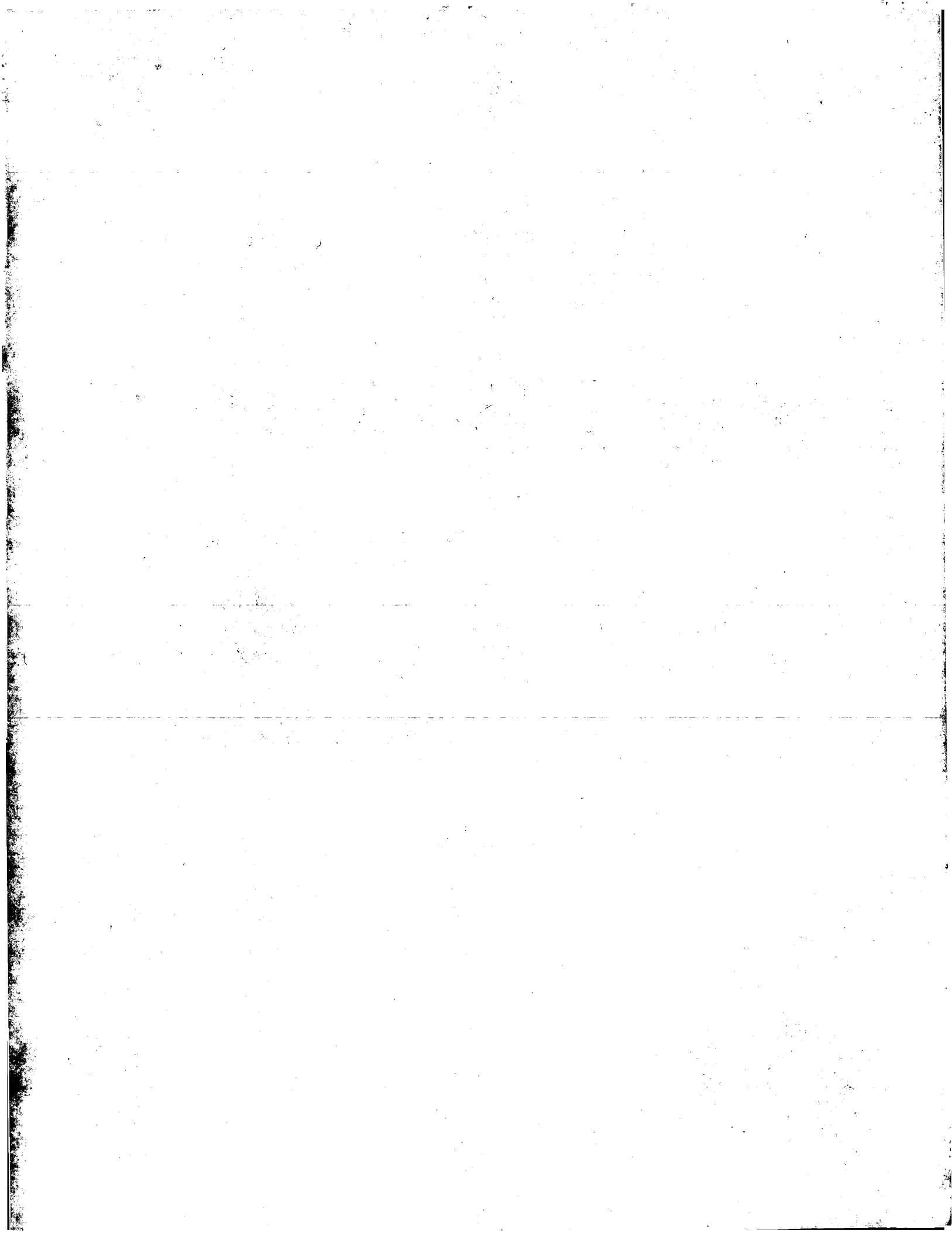
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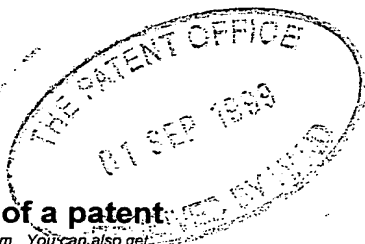
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023EP99 E473741-1 D00056
P01/7700 0.00 - 9920676.5

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Cardiff Road
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1. Your reference

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2. Patent application number
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3. Full name, address and postcode of the or of each patent applicant (underline all surnames)

TAO Group Limited
62/63 Suttons Business Park
Earley
Reading
Berkshire RG6 1AZ
Great Britain

Patents ADP number (if you know it)

06960702002

If the applicant is a corporate body, give the country/state of its incorporation

England

4. Title of the invention

TRANSLATING AND EXECUTING OBJECT-ORIENTED
COMPUTER PROGRAMS

5. Name of your agent (if you have one)

"Address for service" in the United Kingdom
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(including the postcode)

KILBURN & STRODE
20 RED LION STREET
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WC1R 4PJ

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Country

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Date of filing
(day / month / year)

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8. Is a statement of inventorship and of right to grant of a patent required in support of this request? (Answer 'Yes' if:

- a) any applicant named in part 3 is not an inventor, or
- b) there is an inventor who is not named as an applicant, or
- c) any named applicant is a corporate body. See note (d))

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Request for substantive examination (Patents form 10/77) :
Any other documents (please specify) :

11. I/We request the grant of a patent on the basis of this application.

Mr Maggs
Kilburn & Strobe

Signature

Date

1 September 1999

12. Name and daytime telephone number of person to contact in the United Kingdom

Mr. Michael N. Maggs
Tel: 0171-539 4200

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**TRANSLATING AND EXECUTING OBJECT-ORIENTED COMPUTER
PROGRAMS**

5 The invention relates generally to methods of and computer systems for translating (or translating and executing) object-oriented computer programs. More specifically, although not exclusively, the invention is concerned with object-oriented programs in which the code is provided in the form of class files.

10 A well known example of an object-oriented programming language is "Java" (a trade mark of Sun Microsystems Inc.). A "Java implementation" is a software system which allows a software application consisting of one or more class files to be run. These class files must conform to some version of the standard Java Virtual Machine Specification, as published by Sun Microsystems Inc. A class file defines the data and program code required for a particular
15 class.

Although there is some interaction, the Java implementation can conceptually be divided into two parts:

- The Java Virtual Machine (JVM). This reads the class files and executes the instructions contained within them in a way which conforms to some
20 version of Sun's Java Virtual Machine Specification. Some of the instructions contained within a class may reference the data or program code of other classes; the JVM also manages such relationships between classes.
- The Java Class Library. This is a set of predefined classes with data and program code which act in a way conforming to Sun's Java Class Library
25 Specification. These library classes could be implemented in some way other than as real class files, for example using the C or assembler programming languages, in which case the JVM must ensure that references

to their data and program code work in the same way as references to classes which originated from real class files.

The program code in a class file is in an instruction format known as Java bytecode (JBC), or simply as bytecode. Each method in the class has its own
5 sequence of bytecode. The bytecode for a method consists of a sequence of JBC instructions.

There are two schemes which JVMs use to execute the bytecode:

- An interpreter. In this scheme, the JVM contains an interpreter, which is a piece of program code which executes the bytecode by looking at each JBC
10 instruction in turn, decoding it, and performing the actions demanded by it. While this approach is the simplest to implement, its disadvantage is that it is slower than the alternative, since many steps of the interpreter program are required to interpret a single JBC instruction.
- A compiler. In this scheme, the JVM converts the JBC instructions of the
15 bytecode into machine code instructions understood by the CPU being run on (native machine code), before any execution starts. Then, to execute the program code for a method, the compiled machine code is executed instead. There is a time overhead for the initial compilation from JBC instructions to machine code instructions, although this may be done during the preparation
20 of the application rather than when the application is started. Once the compilation has been performed, the method's program code runs much faster, at a speed comparable with other traditionally compiled languages such as C. A special case of the compiler scheme is a just-in-time compiler (JIT), in which the bytecode for a class is compiled just before it is first
25 used.

Some JVMs use a combination of the two schemes, where only program code which is being executed many times is compiled, and the rest is interpreted.

5 Linking is the process by which a reference from one class C1 to another class C2 (or data or a method in C2) is resolved. If C2 is not already loaded, it is loaded, and compiled if using the compiler scheme, and itself linked. Then the reference in C1 to C2 (or some item of data or a method in C2) is modified such that there is now a direct pointer to whatever in C2 is being referred to.

10 Sun's Java Virtual Machine Specification allows for a range of linking schemes:

- Static linking: The loading and linking of all classes of the application is performed when the application is prepared. This scheme is typically used when an application is embedded permanently in a device.
- 15 • Dynamic load time linking: Class C2 is loaded the first time another class is loaded which refers to C2 (or some data item or method within C2).
- Dynamic late binding: Class C2 is loaded the first time a JBC instruction (or its compiled equivalent) which refers to C2 (or some data item or method within C2) is executed.

20 In operation, when a particular method of a particular class is invoked, the particular class required may or may not already be resident in the JVM. If the required class is not resident, then the class file for that class must first be loaded from outside the JVM (for example from a disk or from a network), linked and initialised into the JVM. Then, the required method can be found by looking down the list of methods for the class. Once the required method
25 has been found, the Java bytecode of that method is executed until a return is encountered, whereupon the method has ended and control is returned to the invoker of the method. A method invocation can also be terminated by an

exception being thrown which is not caught in the method.

Figure 1 illustrates a typical prior art implementation in which the JVM makes use of a JIT compiler. The JIT compiler 120 takes the class bytecode 110, just before it is to be used, and translates it into native code 130 ready for execution on a specific processor. The remains of the class 140 (or possibly the entirety of the class) remains available in the memory in case the native code 130 should need to refer to it while running.

Figure 3 illustrates a typical prior art JVM implementation in a multi-processor environment. A server 210 maintains a class store 220 for holding the bytecode of the various classes that may be required by the client processors 230, 240. In this example, the processors 230, 240 are of two different types – namely client type 1 and client type 2 respectively. The server supplies the class files, as necessary, across a communications network generally indicated at 250 to the clients 230, 240. Each of the clients 230, 240 maintains its own JIT, respectively 231, 241, enabling it to compile the class file to its own version of native code, and to store that in its own native code store 232, 242. The native code can then be run on the individual clients.

One problem with the above arrangement is that it requires a large execution environment on each of the clients. JIT compilers are typically large, and it can be difficult if not impossible to provide the requisite space on the client, particularly in an embedded system in which the client consists of, for example, a mobile phone. An alternative approach (not shown) would be to carry out the JIT compilation on the server, and to supply the clients with the native version of each class, as required. While this requires less space on each of the clients, it does have other disadvantages, namely substantial additional complexity at the server. Since each JIT is processor-dependent, the server would in such an arrangement have to maintain a different JIT for each

processor type that might require to be served. While that might be possible in a fixed network, it is unrealistic in for example a mobile phone system where a large number of different phones, of different types and makes, are constantly being connected and disconnected to the server. The owners of the server
5 would have the extremely difficult task of maintaining JITs for all known types of mobile phone, and maintaining and updating those JITs as new phones come onto the market. For that reason, it is preferable for the server simply to maintain a generic class store 220, and for the individual clients to undertake the processor-dependent translations. As mentioned above, however, that has
10 proved difficult to implement in practice because of the limited memories available within embedded systems.

It is an object of the present invention at least to alleviate some of the problems with the prior art, mentioned above.

According to a first aspect of the invention there is provided a method of
15 translating an object-oriented computer program comprising:

- (a) translating the program bytecode into machine independent virtual processor code which uses an instruction set of a virtual processor;
and
- (b) translating the virtual processor code into native code which uses an
20 instruction set of a physical processor.

It will be understood, of course, that in the description and claims the word "code" includes data where that data is part of the program itself. Accordingly, but without limitation, the expression "code" includes such things as constants, variable names and types, flags, pointers, object names and so on.

25 In a preferred embodiment, the object-oriented computer program is written in Java (trademark of Sun Microsystems Inc.), and is designed to be implemented on a Java virtual machine (JVM).

Whether or not Java is used, however, the two-stage translation process of the present invention provides a first stage which is entirely platform-independent and a second stage which is platform-dependent. The Virtual Processor code which results from the first translation is machine-independent, and hence is completely portable. The work required in writing a new translator to translate the Virtual Processor code to a new platform is very significantly less than that which would be required to port the bytecode JIT to the new platform. Where Java is being used, new applications can be written directly in Java, by Java-experienced programmers, who need have no knowledge whatsoever of the Virtual Processor code. A new native translator for translating the Virtual Processor code into native code is needed not for every new application program, but only for every new platform on which the program is to be run. This results in significant time savings for the application programmers. It is envisaged that the native translators will be readily available for each common type of processor, so applications suppliers need only purchase the relevant translators for the processors on which their applications are designed to run; alternatively, the native translators could be supplied as standard with the end user machines themselves.

In one embodiment, the present invention is particularly useful where a common application is to be run on several networked processors. The translation from bytecode to Virtual Processor code may be undertaken on a central server, which may also provide verification of the Virtual Processor class files. Once the Virtual Processor code has been verified, it may be distributed to the individual client processors, either by way of a network broadcast or alternatively on demand. Each client processor maintains its own native translator which it uses to translate the received Virtual Processor code into its own particular variety of native code. The networked client devices

may be heterogeneous, that is they may use different types of processor. That is not a problem in the preferred embodiment of the invention, since each client device will translate into the appropriate native code for its own type of processor.

5 With such an arrangement, the server need only maintain the virtual processor code. It need not know, or care about, which processors or processor types are being used on the networked client devices.

10 The present invention is expected to have particular application in the field of wireless communications, (wireless client networks), and specifically although not exclusively in the field of mobile cellular phone networks. In one preferred embodiment of the invention, the application software on each mobile phone will be automatically updated, in a way which is entirely user-transparent, by downloading the necessary updates in Virtual Processor code from the central server. The downloading could be at regular intervals, or on
15 demand, or as necessary – for example when the user attempts for the first time to use some specific functionality which the phone has not yet been programmed to provide. A similar approach can be taken with other networked devices such as (without limitation) hand-held computers, games consoles, cameras, or indeed any other type of networked or networkable device. In one
20 embodiment the network may consist of or include a wireless network, whereas in other embodiments it may include a private or public fixed network, or the Internet. Where the client devices are not capable of wireless communication, provision may be made for them to be coupled to the Internet as required (for example via a standard modem or ISDN link). In such a way, the invention
25 could be applicable to a wide range of embedded devices, including for example cameras, televisions, washing machines, motor vehicles, or indeed virtually any other type of computer-operated device that can be conceived of.

The invention extends to a computer system for carrying out any of the described methods, and to a corresponding computer program whether or not embodied on a data carrier. The invention further extends to a data stream representative of a computer program for carrying out the described method.

5 The invention may be carried into practice in various ways and one specific embodiment will now be described, by way of example, with reference to the accompanying drawings, in which:

Figure 1 illustrates the operation of a conventional JIT compiler within a JVM;

10 Figure 2 illustrates the two-stage translation process of the preferred embodiment of the present invention;

Figure 3 shows a typical prior art client/server system;

Figure 4 illustrates the operation of the preferred embodiment of the invention within a client/server system;

15 Figure 5 illustrates the operation of the present invention within a wireless network; and

Figure 6 illustrates certain aspects of the translation from bytecode to intermediate virtual processor code according to the preferred embodiment of the invention.

20 Figure 1, which has been described above, shows the way in which a JIT compiler within a JVM translates from processor-independent bytecode to processor-dependent native code for running on a particular processor. In the present invention, conversion from bytecode to native code takes place in two separate stages:

25 1. Conversion from the class file to an intermediate processor-independent form. This will be referred to as Virtual Processor or VP code. The converter itself is known in the preferred implementation as the "jcode

translator”.

2. Conversion from the intermediate VP form to the native machine code. The converter here will be known as the “native translator”.

5 Figure 2 illustrates in more detail the translation from class bytecode to native code. The class byte code 210 is first checked for validity by a class verifier 211. This checks not only the individual bytes themselves, but also checks for valid external and internal references. The class verifier if necessary loads additional classes to check the external references.

10 Once the code has been checked, it is passed to the jcode translator 212 which converts it, as described in more detail below, into VP code 213. The VP code 213 is then converted by the native translator 214 to the native code 230.

15 It is important to appreciate that the class verifier 211, the jcode translator 212 and the VP code 213 are all processor-independent. It is only the native translator 214 and of course the final native code 230 which is processor-dependent.

The use of the preferred embodiment within heterogeneous multi-processor environment is shown schematically in Figure 4. This should be compared with the corresponding prior art approach shown in Figure 3.

20 In Figure 4, the server 410 is serving two clients 430, 440 (having different processors) via a communications network 450. All of the processor-independent calculation is carried out on the server; in particular, the server maintains a class store 420, a class verifier 421, a jcode translator 422 and a VP store 423. The VP (processor-independent) code can then be served, as
25 required, across the network 450 to the individual clients. The VP code is then translated by the individual client translators 424, 425 and the appropriate native code for the specific processor stored within the native code stores 432,

442.

The use of VP on the server, as shown in Figure 4, allows the verification of the class files and the first stage of the compilation (the conversion to VP code) to be performed once only by the server. Then, only the native translation (which differs according to the processor type) needs to be performed by the client device before execution. Such an arrangement makes it easy to supply updated classes at the server, without the server needing to know anything about the details of the particular clients that will wish to make use of those classes. An updated class needs to be amended once only, in the class bytecode, and then translated once only into VP. The VP is transmitted to the client devices, as necessary, and the final translation to native code can be carried out at the client in a way which is entirely transparent to the end user. In addition, no amendment to the server or to the VP code is required in the event that a new type of client comes onto the market which requires different native code. The client manufacturer simply provides the client with an appropriate native translator, and the device should operate without any manual intervention at the server.

Once specific implementation, illustrated in Figure 5, is that of a mobile phone network. Individual mobile phones 530, 550 using the network each include a respective native translator 524, 525 and a native code store 532, 542. When it is required to upgrade the functionality of the phones, updated VP code is supplied from a VP store 523 on a central server 520. The updated VP code is sent via a land-based communications network 511 to a wireless transmitter 512. The code is then packetized and sent across a wireless link 513 to the individual phones. On receipt, the VP code is automatically translated into native and stored in the native code store. The whole process may be transparent to the phone user; or alternatively the updated code may be sent on

receipt of a specific request from the phone user, via the wireless link 513.

Turning back now to Figure 2, further details will be given of the two-stage translation from class bytecode 210 into native code 230. As previously described, the class verifier 211 checks the class bytecode for validity. The
5 class verifier may in some embodiments be incorporated within the jcode translator, in which case the class bytecode 210 is passed straight to the jcode translator 212 as shown by the arrow 240.

The JVM and the bytecode instructions it implements are stack based, which means that operands (numbers, pointers to objects) are kept on a stack,
10 on which the last item to be pushed on is the first to be popped off. A bytecode instruction typically removes one or more operands from the stack, performs some action, and pushes the result operand (if any) back on the stack. On the other hand, VP is register based, in that it has a set of registers which are addressed directly by the VP instructions. An instruction typically takes its
15 operand(s) from register(s) specified in the instruction, performs some action, and puts the result operand (if any) into a further register specified in the instruction. This register based architecture is more similar to most real processors, except that VP has a very large number of registers, large enough such that any system converting to VP does not need to worry about how many
20 there are.

VP instructions are based around expressions. A single instruction typically has one or two operands, and each operand can be a constant, a register, or an expression. An expression then has one or two operands, each of which can be a constant, a register or an expression. In this way, an arbitrarily
25 complex instruction can be built up.

There now follows a more detailed description of how parts of a class file are converted. The description uses the term "fixup"; this is a small item

of data attached to a particular point in the compiler's output code or data which instructs the JVM that the code or data at that point needs to be modified in some way before it can be used. Fixups are used to change a native instruction or a data item such that the native code can obtain a direct reference to another class, or to a field or method therein.

A java class file consists of the following parts:

- A constant pool, which contains the constant numbers and names in other parts of the class file, instead of a name, there is a reference to a name which is stored here.
- Information such as the name of this class, the superclass and any direct superinterfaces.
- A list of fields, with information on each one.
- A list of methods, with information on each one. This information includes its code section. Thus there are several code sections, one for each method.

The Java class file is converted to VP tools as follows:

- A data tool. Despite its name, this has nothing to do with the data to be used by the class. Instead it contains information about the fields and methods which is used by classes in the package `java.lang.reflect` (often known as "reflect" for short) in the library. Reflect allows one to enumerate and manipulate the fields and methods in a class.
- A class tool. This contains some housekeeping information used by the JVM (including the size of object to allocate, the size of the class's static data if any, and the superclass and superinterfaces), and code for none, some or all of the methods.
- Zero or more method tools. Methods which do not appear in the class tool

have their own individual tools. The decision on whether to place a method in its own tool can be based on a number of factors such as the size of the method.

- A fixup tool. The fixup tool returns a constant fixup value which is used to determine the offset within an object of a particular field. The tool is called at fixup time to provide the offset, and the binder/linker patches this offset into the code that wants to use it.. It is thus used to implement both “get a field” and “put a field” in the bytecode.

The data tool can be discarded if the java application is known not to use certain facilities (largely reflect), and the fixup tool can be discarded if the java application is to be embedded in a device which does not dynamically load further java classes.

The jcode translator uses a VP register for each item on the stack.

VP code does not directly implement the class file’s mechanisms for accessing another class, method or field from within the bytecode. In bytecode, there are instructions for: Call a method (in this or another class), Get the contents of a field (in this or another class), pushing those contents onto the stack, Pop an item off the stack and put into a field (in this or another class). The jcode translator converts these instructions to VP instructions which do the following:

- Call a non-static method (i.e. one to which an object pointer must be passed) in a class. VP has the concept of a class with methods, which is used to implement Java classes. Such methods can be called virtually (the actual method called depends on the class of the object whose pointer is passed) or non-virtually (the method called is in the class specified in the call).

- Call a subroutine. This is used to implement the bytecode's call of a static method (i.e. one to which no object pointer need be passed), and in some cases a non-static method.
- Get the value of the constant fixup from the fixup tool.

5 The constant pool within a class file is converted as follows:

- A constant pool entry containing a constant number (integer or floating point) is incorporated into the compiled version of the JBC instruction which references the constant number.
- A constant pool entry containing string data which is used directly by a JBC instruction is copied into the data attached to the compiler's output code.
- Other constant pool entries containing string data are not used directly, but are used when referred to by the constant pool types below, or by other parts of the class file.
- A constant pool entry referencing a class C causes a fixup referencing the class C (or the JVM's internal name for the class) to be attached to the compiler's output code/data such that a JBC instruction using this constant pool entry to refer to C is compiled to a native code sequence which, after applying the fixup, obtains access to class C's code and data.
- A constant pool entry referencing a field F in a class C causes a fixup referencing F in C (or the JVM's internal name for F in C) to be attached to the compiler's output code/data such that a JBC instruction using this constant pool entry to refer to F is compiled to a native code sequence which, after applying the fixup, obtains access to field F.
- A constant pool entry referencing a method M in a class C causes a fixup referencing M in C (or the JVM's internal name for M in C) to be attached to the compiler's output code/data such that a JBC instruction using this

constant pool entry to refer to M is compiled to a native code sequence which, after applying the fixup, obtains access to method M.

- A constant pool entry giving a name and type of a field or method is not used directly, but is used when referred to by other constant pool entry types or other parts of the class file.

The code section within a class file is converted as follows:

- Code doing purely numerical calculations (ie where there is no reference to an external method) is translated straight from bytecode into a corresponding tool in VP.
- As shown in Figure 6, where the bytecode 600 has a reference 610 to a field, that is converted at fixup time by a call 611 to the fixup tool. The call to the fixup tool returns a value which references the location of the field. Thus, by the time the instruction is run it has been patched to contain the correct offset.
- A static method 620 is converted to a corresponding VP tool, but with added fixup code 621.
- A non-static method 630 has added to it a fixup for a method call (ie a reference to the method name). This will eventually become an atom in the final native code.

Other parts of the file are converted as follows:

- The name of the class determines the name used by the JVM to refer to the code and data output by the compiler.
- The name of the superclass becomes some reference to the superclass within the code and data output by the compiler. In the preferred implementation, the output data contains a pointer with a fixup attached such that, after linking, the pointer points to the superclass code and data.

- The name of each interface becomes some reference to the interface within the output code and data. In the preferred implementation, the output data contains a pointer for each interface with a fixup attached such that, after linking, the pointer points to the interface code and data.
- 5 • The debug information attached to each method (and the source filename which is stored in the class file), when present, is converted to a format suitable for the environment in which the JVM is running. In the preferred implementation, the debug information is converted to the same format used for non-Java parts of the system.

10 The final VP class comprises one or more named tools, normally including at least the data tool, the class tool, the fixup tool and zero or more method tools. The tool names are generated automatically by the jcode translator, each name being related to the name of the class and the function of each tool within the implementation of that class.

15 Turning back again to Figure 2, further details will now be given of the native translator which translates the VP code into native code. It will be understood, of course, that VP code is never itself run directly in a live application; it is always converted by the processor-dependent native translator into the appropriate native code for the processor on which it is to be executed.

20 The native translator 214 is quite a small piece of code (around 150k, depending upon the processor), so that it can easily be stored in memory within an embedded system. The translator 214 maps VP registers to the registers of the particular processor being used. The translator uses its knowledge of the real processor's register architecture to decide at each point in the output native
25 code which VP registers should be mapped to the real processor's registers, and which should be kept in memory (which is slower to access). The translator also provides machine-dependent optimisation of instructions. Until the native

code is bound in, it will still normally contain sections of fixup code. On binding (or sometimes at run-time) the fixup code will be replaced with appropriate machine-dependent instructions. For example, the fixup for a non-static method will be converted to an atom in the native code.

5 Both the jcode translator and the native translator are themselves preferably written in VP code and can thus be translated (using the native translator itself) to run on any desired platform. From that initial VP code, compiled versions of both translators may be provided in native code, optimized for the particular processor on which the translator is to execute. To compile
10 the VP code for the jcode translator, that code is passed through the native translator. To compile the VP code for the native translator, that code is passed through the native translator itself.

 Although the preferred embodiment uses the Java Virtual Machine, the overall inventive concept is more general, and it is not essential to use the JVM,
15 or indeed Java at all. Where Java is used, however, the invention described allows Java-skilled applicators programmers to develop programs in their preferred language, without having to understand, or even to know anything about, VP code. The only requirement is that there may be a native code translator available for each physical processor on which the application is to be
20 run. It is envisaged that such native translators will be generally available, and appropriate translators could either be purchased by the application developer or alternatively provided as standard on individual client devices such as mobile phones or games consoles.

CLAIMS:

1. A method of translating an object-oriented computer program comprising:

- 5 (a) translating the program bytecode into machine independent virtual processor code which uses an instruction set of a virtual processor; and
- (b) translating the virtual processor code into native code which uses an instruction set of a physical processor.

10

2. A method as claimed in claim 1 in which the program bytecode includes a class file, the class file being converted into one or more virtual processor tools which use the instruction set of the virtual processor.

15

3. A method as claimed in claim 2 in which the class file includes a plurality of methods, and which some or all the methods in the class file are converted to a respective virtual processor tool.

20

4. A method as claimed in claim 2 in which the class file includes a call to a method, and in which the virtual processor code provides a call to a corresponding tool.

25

5. A method as claimed in claim 2 in which the class file includes a reference to a field, and in which the virtual processor code provides a fixup tool for use in locating the field.

6. A method as claimed in claim 5 in which the fixup tool is arranged to

return a constant fixup value which is representative of the offset of the said field within an object.

5 7. A method as claimed in claim 6 including linking the virtual processor code and determining the constant fixup value in dependence upon virtual processor code which has been translated from another class file.

8. A method as claimed in claim 6 or claim 7 in which the fixup tool returns a value which is used to patch a method which gets or puts the value of a field.

10

9. A method as claimed in claim 2 in which the virtual processor code has, included within it at a plurality of points, fixup instructions which indicate that the code at the said points has to be modified by the respective fixup instruction prior to use.

15

10. A method as claimed in claim 7 in which the fixup instructions provide instructions as to how the native code can reference another class, or a field or method in another class.

20

11. A method as claimed in claim 9 or claim 10 in which the fixup instructions are transferred, substantially functionally unaltered, by the native translator into the native code; the fixup instructions being replaced with native instructions when the native code is bound on the said real physical processor.

25

12. A method as claimed in any one of claims 1 to 11 in which the bytecode is stack-based, and in which the virtual processor code is register-based.

13. A method of executing an object oriented computer program comprising translating the program into native code as claimed in any one of the preceding claims, and executing the native code on the physical processor.

5 14. A method as claimed in claim 13 when dependent upon claim 2 including binding the translated tools into a task, and executing the task in native code on the physical processor.

10 15. A computer system adapted to carry out a method as claimed in any one of the preceding claims.

16. A method as claimed in any one of claims 1 to 12 which further includes:
(c) translating the virtual processor code into a different native code
which uses an instruction set of a second physical processor.

15

17. A method as claimed in claim 13 or claim 14 including executing the different native code on the second physical processor.

20 18. A computer system adapted to carry out a method as claimed in claim 16 or claim 17.

19. A distributed computer system comprising a server including a store for storing virtual processor code, said code being a machine-independent representation of an object oriented computer program, and a plurality of
25 remote client devices in communication with the server, each client device including a client processor, a native translator arranged to translate the virtual processor code into native code which uses the instruction set of the respective

client processor, and a native code store; the system including transmission means for transmitting the virtual processor code from the server to the client devices.

5 20. A distributed computer system as claimed in claim 19 in which the transmission means consists of or includes a wireless network.

21. A distributed computer system as claimed in claim 20 in which the client devices are mobile phones.

10

22. A distributed computer system as claimed in claim 20 in which the client devices are hand-held computers.

15

23. A distributed computer system as claimed in claim 19 or claim 20 in which the client devices are hand-held games consoles.

20

24. A distributed computer system as claimed in claim 19 in which at least one of the client devices includes a first type of client processor and in which at least another of the client devices includes a second type of client processor, using a different instruction set from that of the first type.

25

25. A distributed computer system as claimed in any one of claims 19 to 24 in which the server is further arranged to translate the object-oriented computer program from bytecode into virtual processor code.

26. A method as claimed in any one of claims 2 to 11, or claim 12 when dependent upon claim 2, including verifying the integrity of the class bytecode,

and of any external calls.

27. A method as claimed in any one of claims 2 to 11, or claim 12 when dependent upon claim 2, in which the class file is a Java class file.

5

28. A method as claimed in any one of claims 1 to 12, 26 or 27 in which the step of translating the program bytecode into virtual processor code is carried out by a first translator program which is itself written in virtual processor code.

10

29. A method as claimed in any one claims 1 to 12, 26, 27 or 28, in which the step of translating the virtual processor code into native code is carried out by a second translator program which is itself written in virtual processor code.

15

30. A computer program for executing a method as claimed in any one of claims 1 to 12 or 26 to 29.

31. A data carrier which carries a computer program for executing a method as claimed in any one of claims 1 to 12 or 26 to 29.

20

32. A data stream representative of a computer program for executing a method as claimed in any one of claims 1 to 12 or 26 to 29.

25

ABSTRACT

5 A method of translating object-oriented computer programs into native
code includes a two-step translation process, first by a jcode translator (212)
into a machine-independent virtual processor code (213), and then by a native
translator (214) into native code (230). In a heterogeneous multiprocessor
environment, the machine-independent part of the translation can be carried out
10 at the central server, with machine-independent virtual processor code being
distributed as required to the individual clients. Each client then needs to carry
just a small processor-dependent native translator (214) with which it completes
the conversion into native code. The invention finds particular application in
embedded systems, and particularly in heterogeneous-processor systems such as
15 mobile phone networks.

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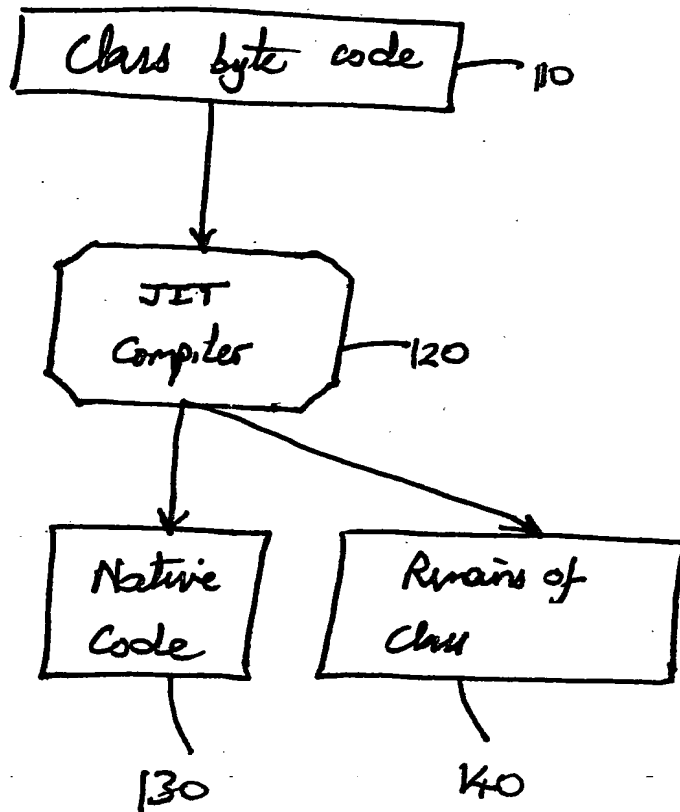


Figure 1

Prior ART

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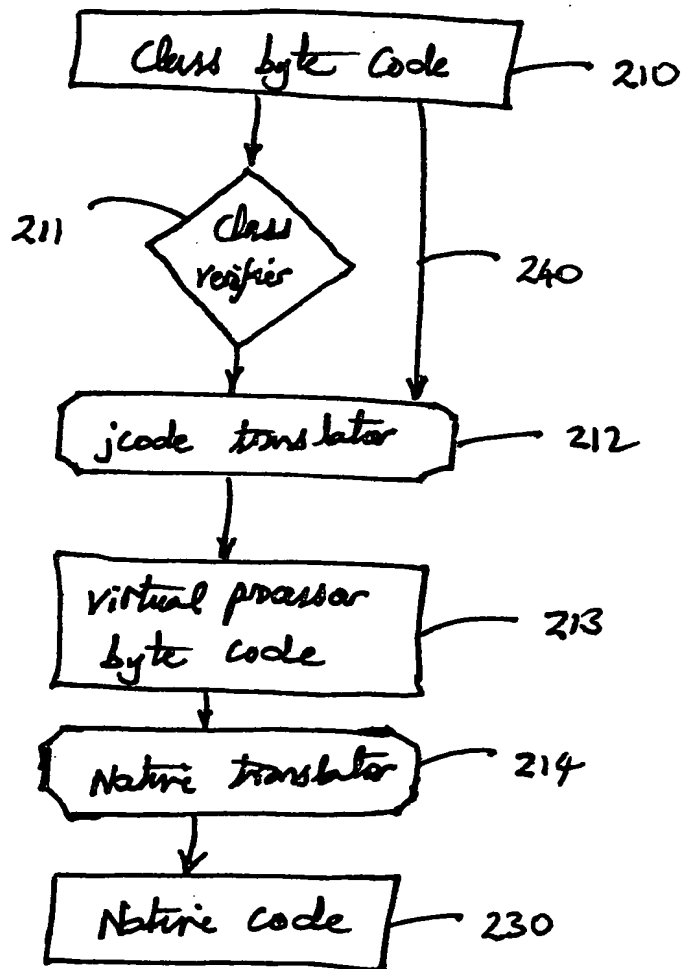


Figure 2

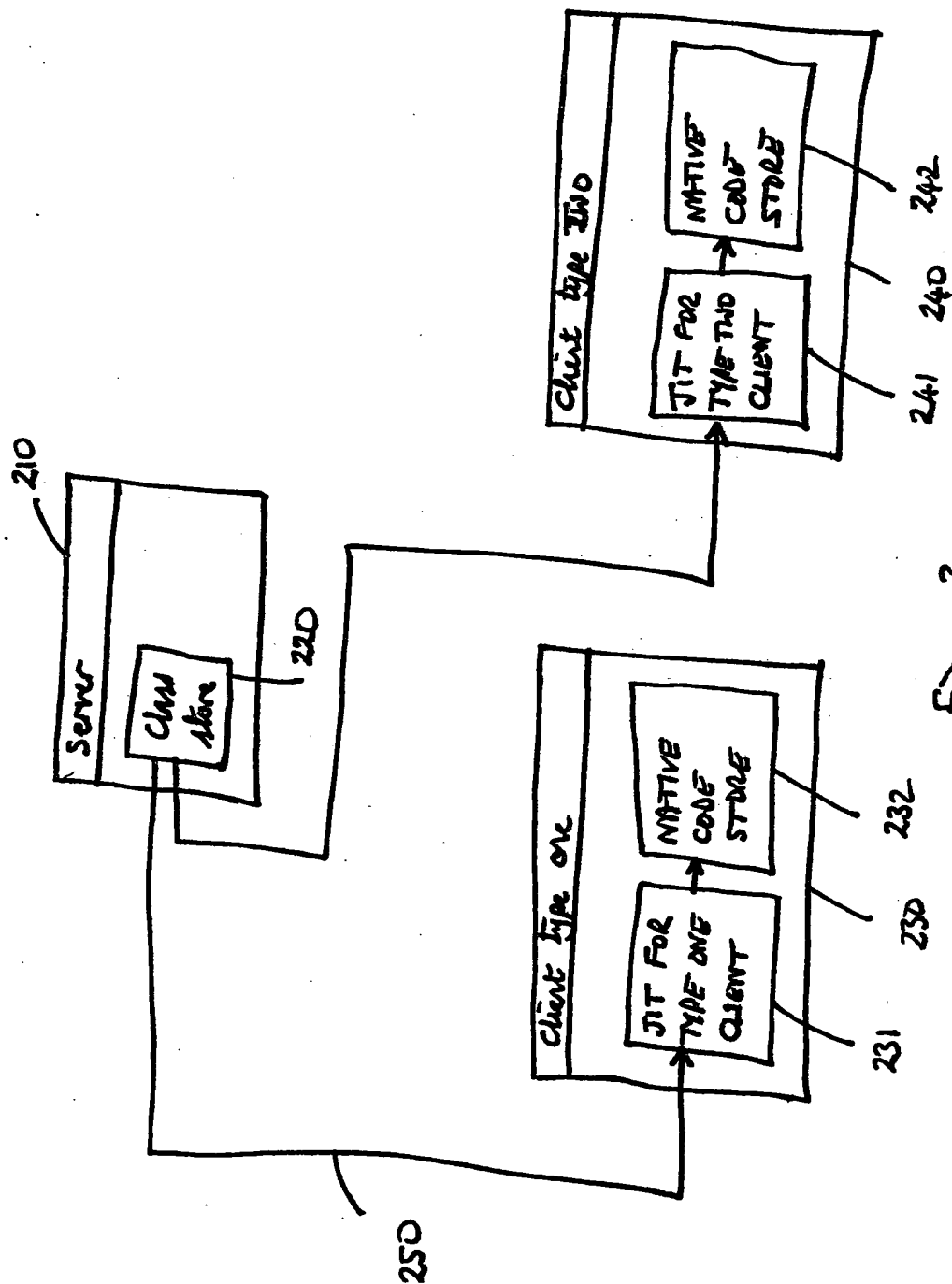


Figure 3
PRIOR ART

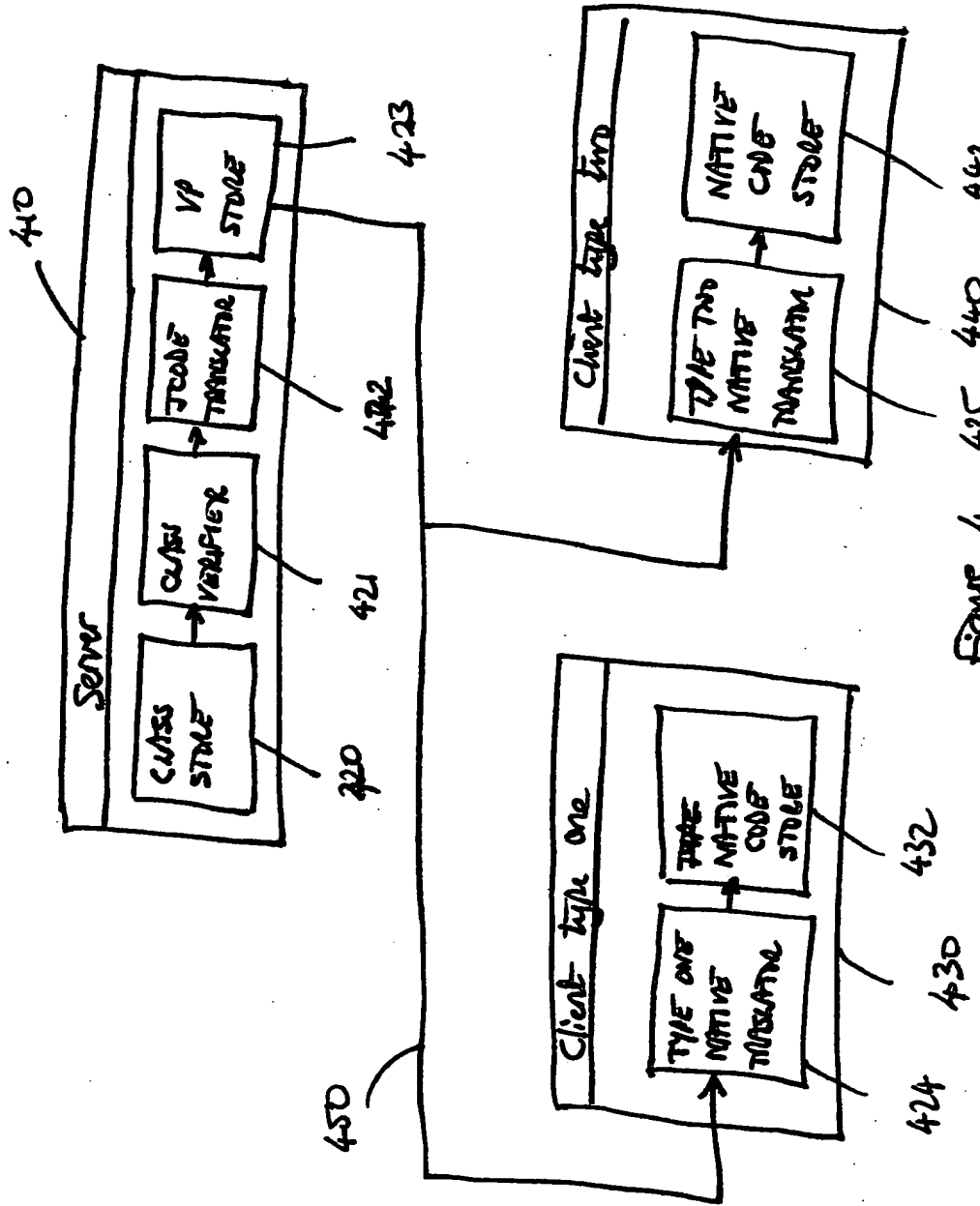


Figure 4. 425 440 442

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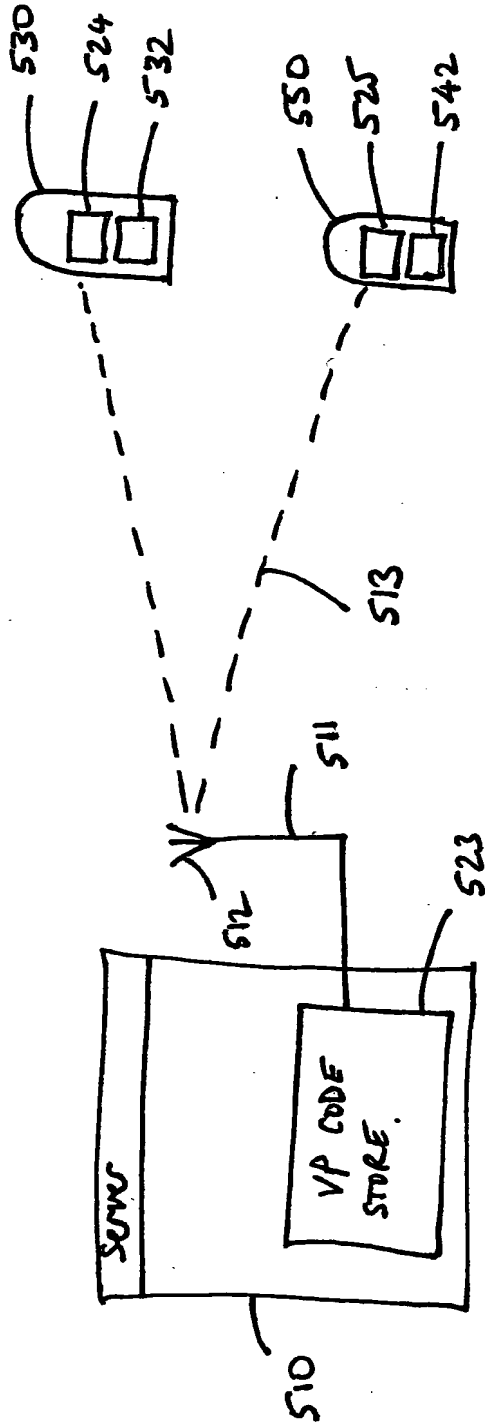


Figure 5

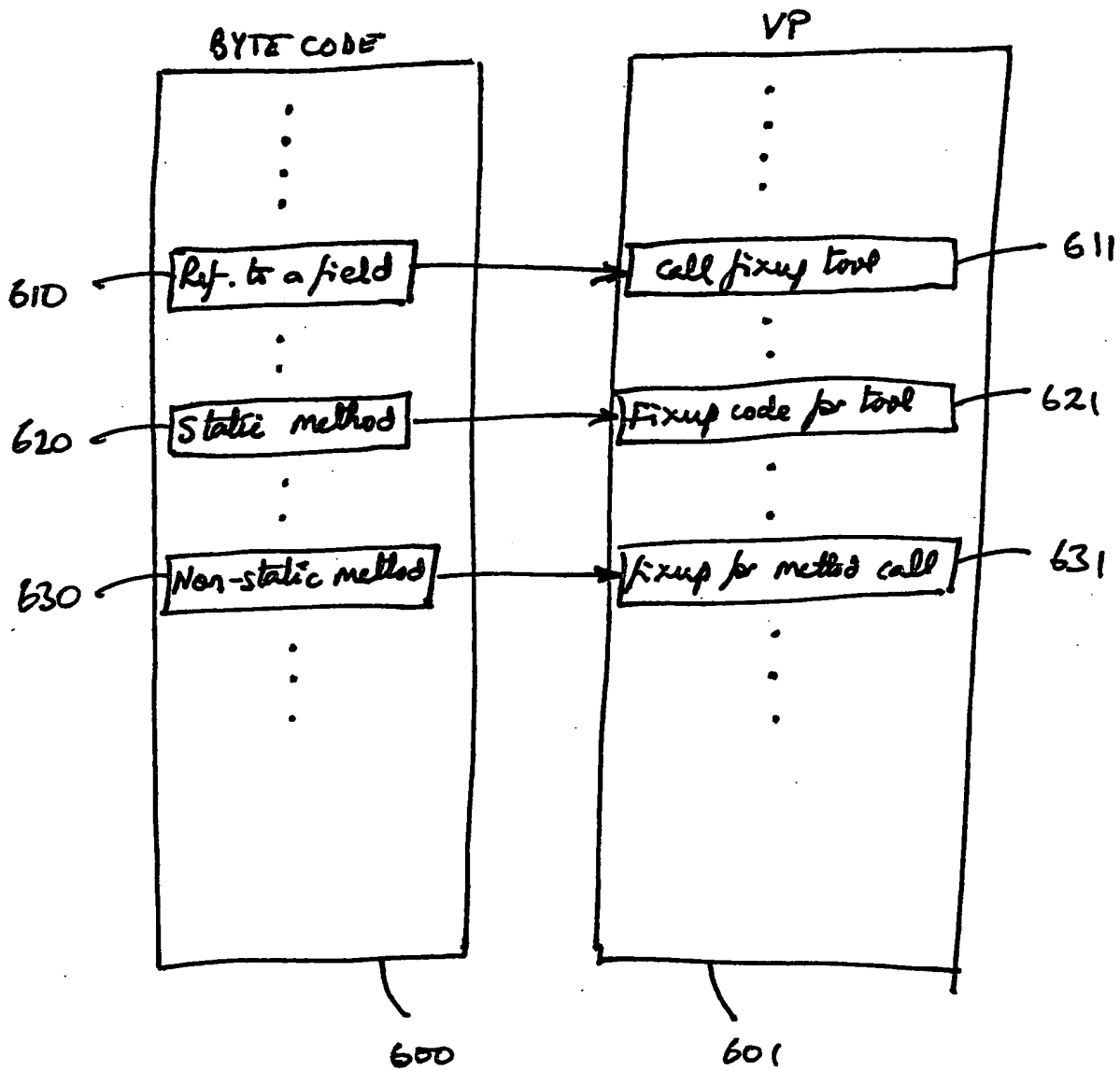


Figure 6

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